

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): William N. Joy et al.

Title: SWITCHING METHOD IN A MULTI-THREADED PROCESSOR

Application No.: 10/779,944

Filed: February 17, 2004

Examiner: Eddie P. Chan

Group Art Unit: 2183

Atty. Docket No.: 004-3878-2

July 16, 2004

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- ☒ Form(s) PTO-1449 (2 pages), including copy(ies) of n/a reference(s).
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

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- 1 -

Application No.: 10/779,944

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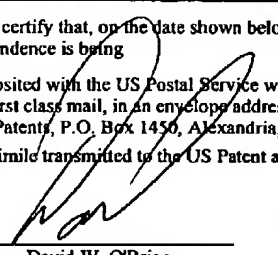
PREVIOUSLY SUBMITTED OR CITED REFERENCES

Certain references listed on the attached Form(s) PTO-1449 were previously submitted or cited in parent Application No. 10/074,419, filed February 12, 2002. In accordance with C.F.R. §1.98(d), copies of those references are not supplied herewith.

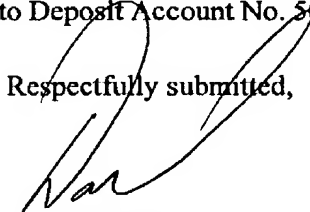
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

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 David W. O'Brien	<u>16-July-04</u> Date

Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-3878-2		
				Application No.: 10/779,944		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s): Joy et al.		
(Use several sheets if necessary)				Filing Date: February 17, 2004		
				Group Art Unit: 2183		
				Date Submitted: July 16, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	1	3,728,692	4/1973	Fennel, Jr.		
	2	5,361,337	11/1994	Okin		
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Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
Examiner			Date Considered			
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NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
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	20	Byrd, G. T. et al., "Multithreaded Processor Architectures", IEEE Spectrum, Vol. 32, No. 8, pages 38-46, August 1995.
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Examiner		Date Considered
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